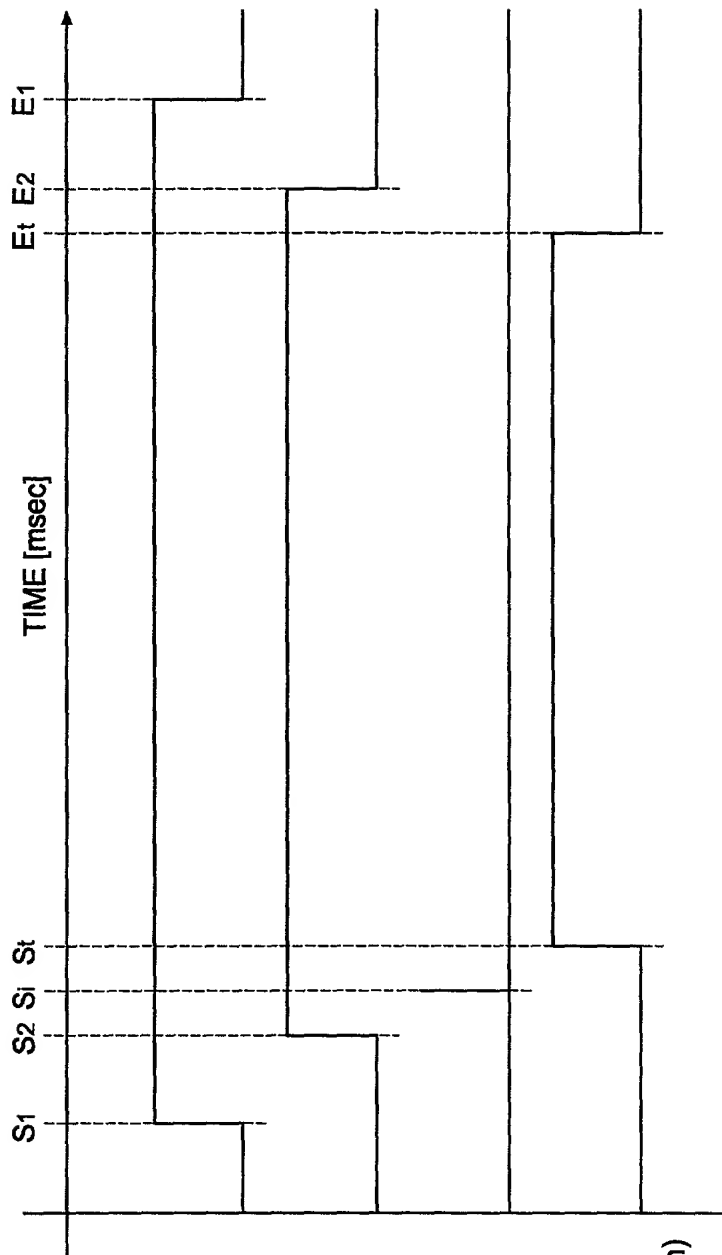


Fig. 1A
(DUT Power #1)

Fig. 1B
(DUT Power #2)

Fig. 1C
(DUT Initialization)

Fig. 1D
(Digital Test Pattern)



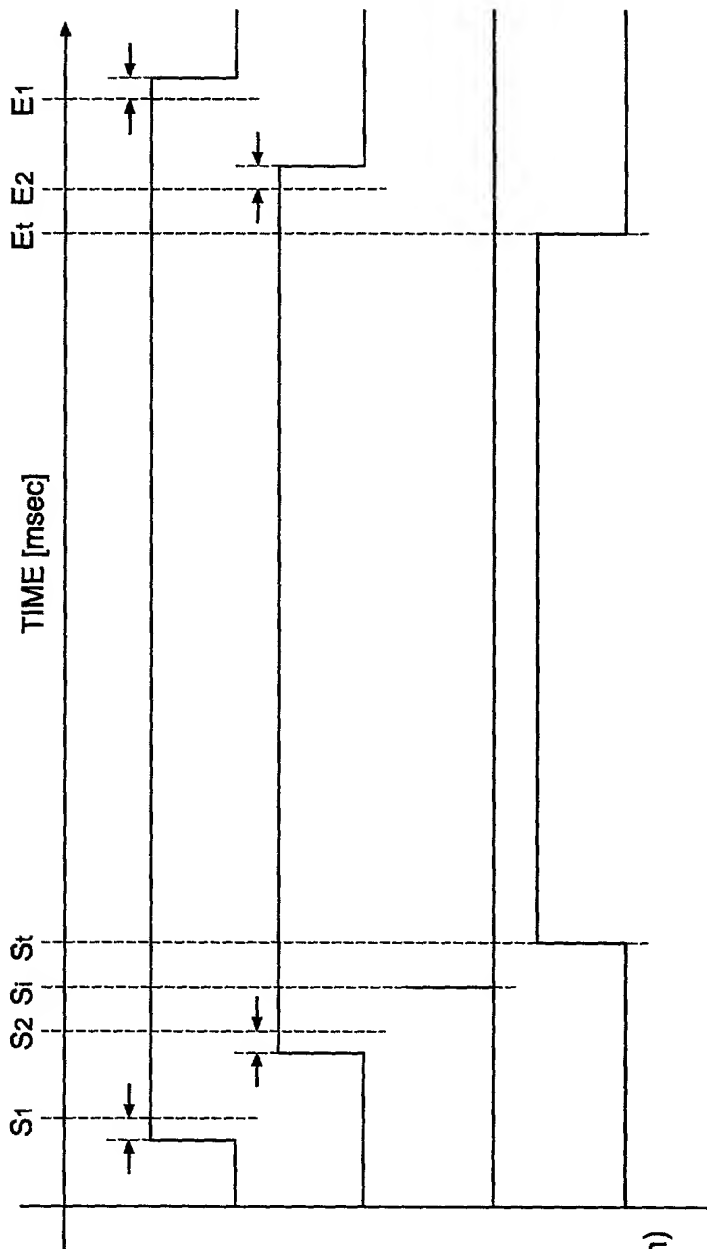


Fig. 2A

(DUT Power #1)

Fig. 2B

(DUT Power #2)

Fig. 2C

(DUT Initialization)

Fig. 2D

(Digital Test Pattern)

Fig. 3

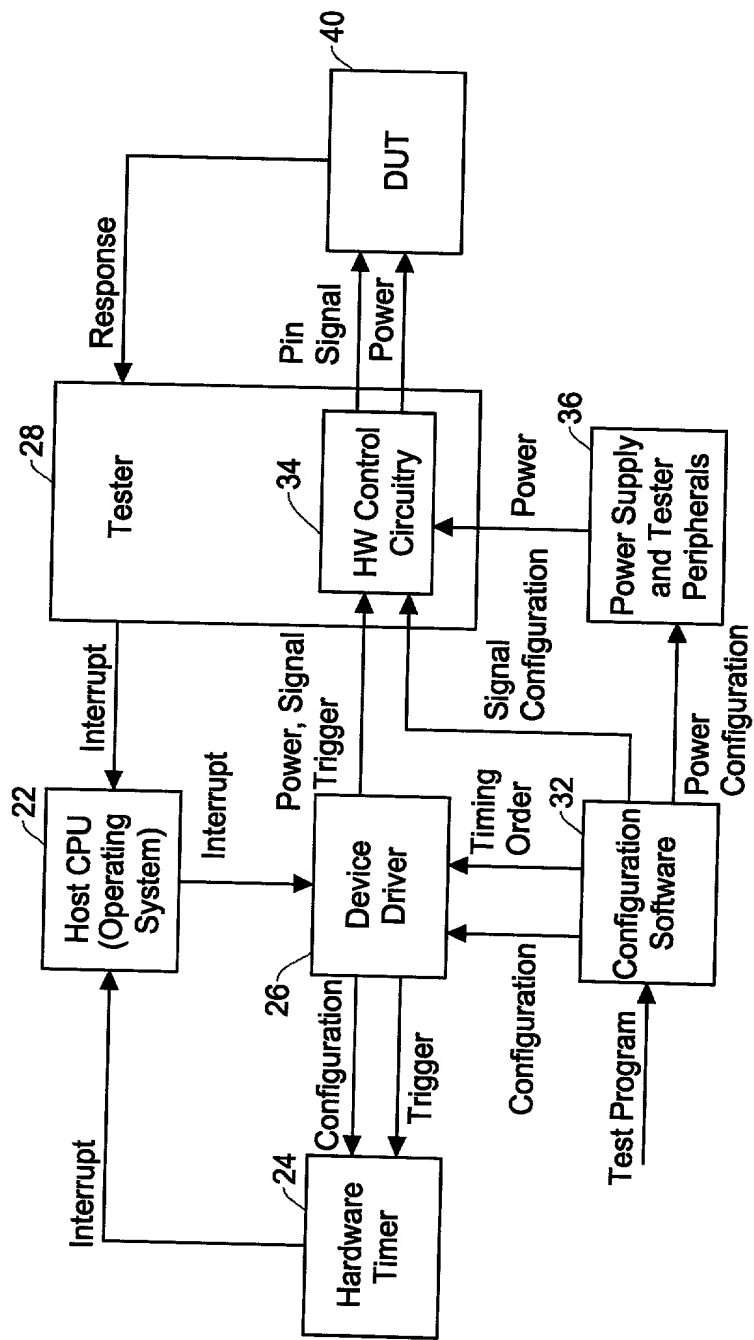
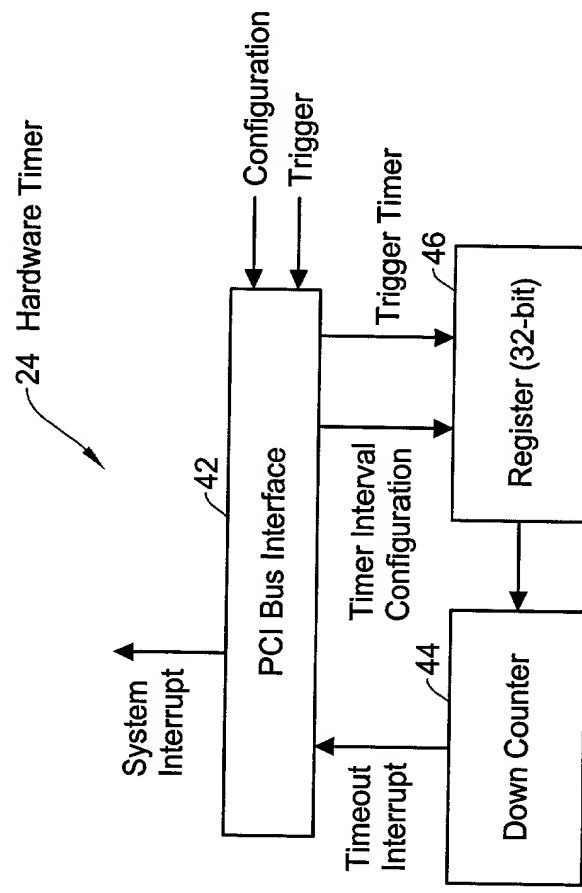


Fig. 4



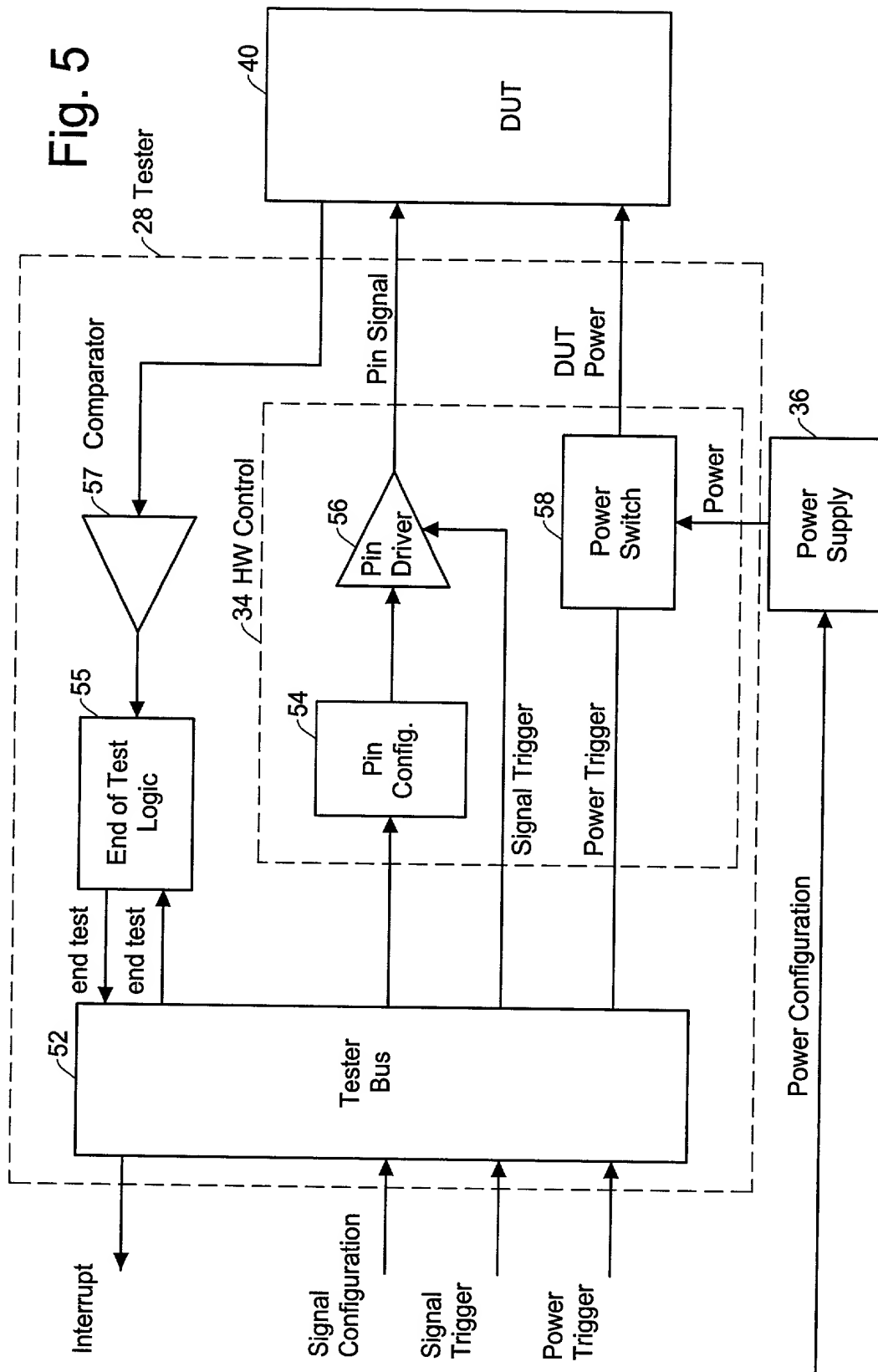


Fig. 6

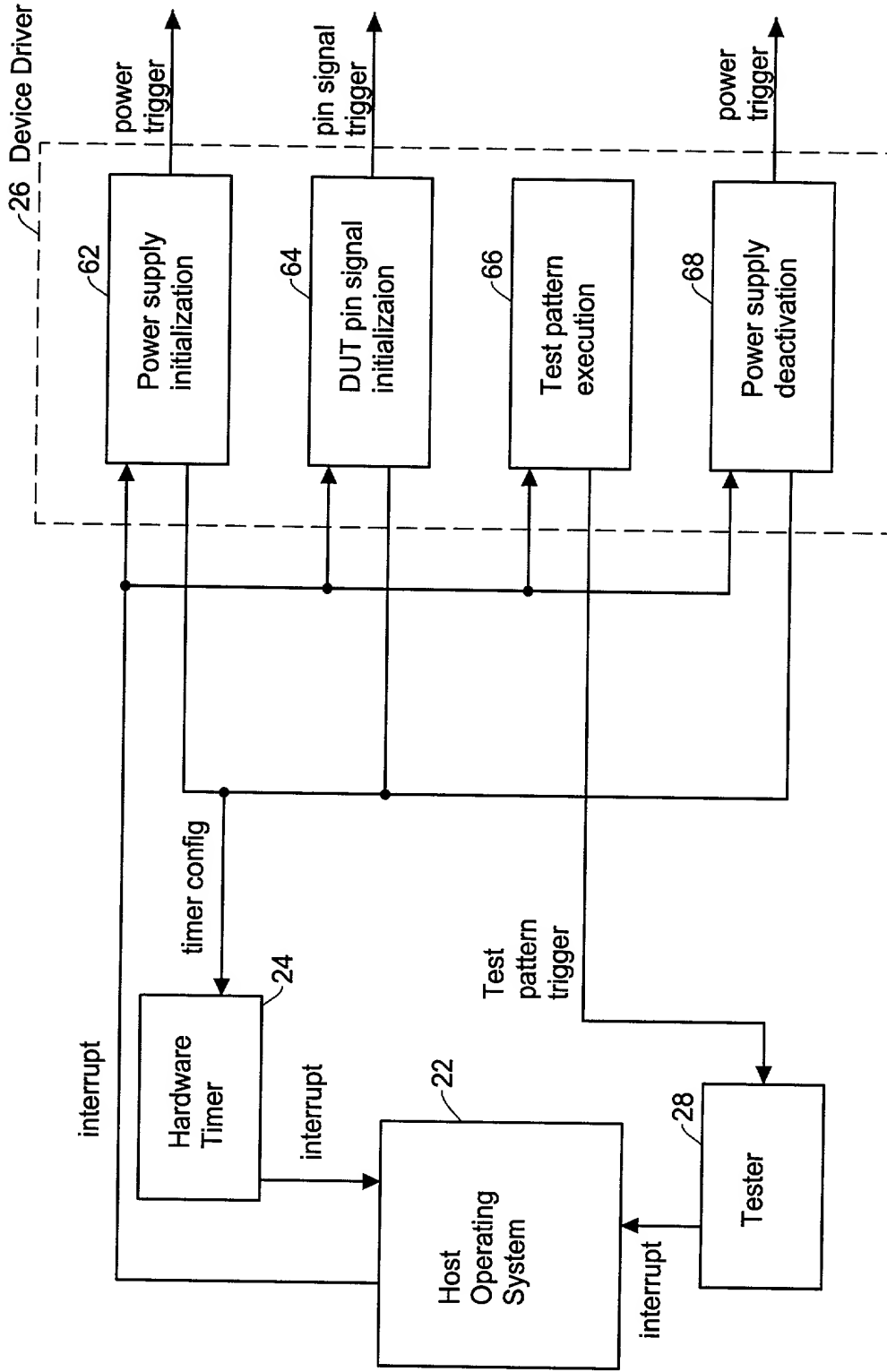


Fig. 7

